

\*RESPONSE UNDER 37 CFR 1.116-EXPEDITED PROCEDURE EXAMINING GROUP\_ ユ & ユ と \_ "

Docket No.: 240708US2

COMMISSIONER FOR PATENTS
ALEXANDRIA, VIRGINIA 22313

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RE: Application Serial No.: 10/625,583

Applicants: Kenichi HAYASHI et al.

Filing Date: July 24, 2003

For: SEMICONDUCTOR DEVICE AND

SEMICONDUCTOR ASSEMBLY MODULE

Group Art Unit: 2826

Examiner: WILLIAMS, A. O.

SIR:

Attached hereto for filing are the following papers:

## REQUEST FOR RECONSIDERATION UNDER 37 C.F.R. § 1.116

Our check in the amount of - is attached covering any required fees. In the event any variance exists between the amount enclosed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,

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